

**AMENDMENTS TO THE CLAIMS**

1. (Currently Amended) A circuit board, comprising:  
a first conductor having a width (W) disposed between a first point and a second  
point of said circuit board; and  
a first plurality of compensation tabs having said width (W) on said conductor;  
wherein said first plurality of compensation tabs are operable to cause said  
conductor to have electrical conduction properties comparable to a  
conductor having a longer physical length than said first conductor.

Claims 2-4 (Cancelled)

5. (Currently Amended) The circuit board according to claim 1, further  
comprising a second conductor having said width W aligned substantially parallel to said  
first conductor, said second conductor having a second plurality of compensation tabs  
having said width W thereon,  
wherein said first plurality of compensation tabs and said second plurality of  
compensation tabs are aligned in an interleaved pattern in the area  
between said first and second conductors.

6. (Currently Amended) A method of forming a conductor on a circuit board,  
comprising:  
forming first conductor having a width (W) on said circuit board between a first  
point and a second point, thereof; and  
forming a first plurality of compensation tabs having said width W on said first  
conductor;

wherein said first plurality of compensation tabs are operable to cause said conductor to have electrical conduction properties comparable to a conductor having a longer physical length than said first conductor.

Claims 7-9 (Canceled)

10. (Currently Amended) The method according to claim 6, further comprising wherein a second conductor having said width W is aligned substantially parallel to said first conductor, said second conductor having a second plurality of compensation tabs having said width W thereon,

wherein said first plurality of compensation tabs and said second plurality of compensation tabs are aligned in an interleaved pattern in the area between said first and second conductors.

11. (Currently Amended) An information handling system, comprising: at least one circuit board comprising information processing circuits and signal conductors, said circuit board further comprising:

a first conductor having a width W disposed between a first point and a second point of said circuit board; and  
a first plurality of compensation tabs having said width W on said conductor; wherein said first plurality of compensation tabs are operable to cause said conductor to have electrical conduction properties comparable to a conductor having a longer physical length than said first conductor.

Claims 12-14 (Canceled)

15. (Currently Amended) The information handling system according to claim 11, further comprising a second conductor having said width W aligned substantially parallel to said first conductor, said second conductor having a second plurality of

compensation tabs having said width W thereon,  
wherein said first plurality of compensation tabs and said second plurality of  
compensation tabs are aligned in an interleaved pattern in the area  
between said first and second conductors.

16. (Currently Amended) A method of forming conductors in an information handling system, said information handling system including a circuit board comprising information processing circuits and a plurality of conductors, said method comprising:  
forming first conductor having a width (W) on said circuit board between a first point and a second point, thereof; and  
forming a first plurality of compensation tabs having said width W on said first conductor;  
wherein said first plurality of compensation tabs are operable to cause said conductor to have electrical conduction properties comparable to a conductor having a longer physical length than said first conductor.

Claims 17-20 (Canceled)

20. (Currently Amended) The method according to claim 16, ~~further comprising wherein~~ a second conductor having said width W is aligned substantially parallel to said first conductor, said second conductor having a second plurality of compensation tabs thereon,  
wherein said first plurality of compensation tabs and said second plurality of compensation tabs are aligned in an interleaved pattern in the area between said first and second conductors.